

FIG.1

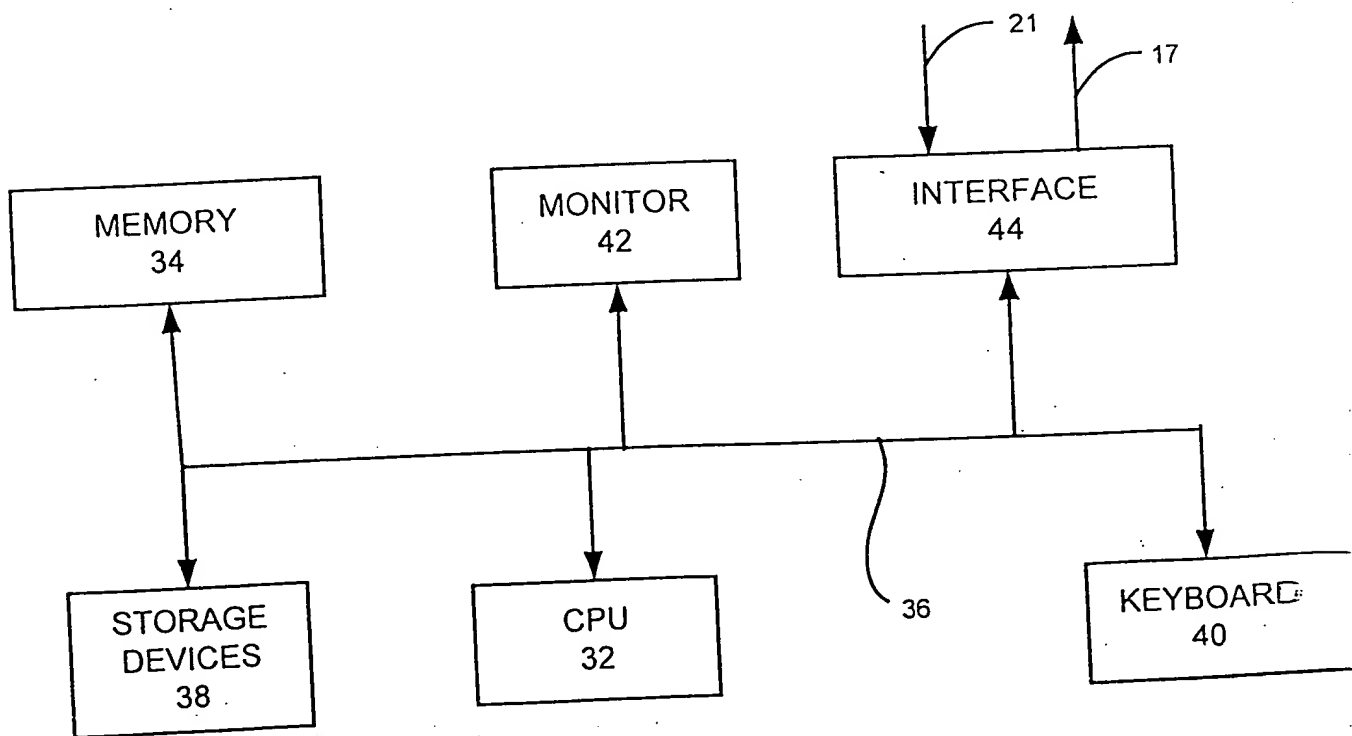


FIG.2

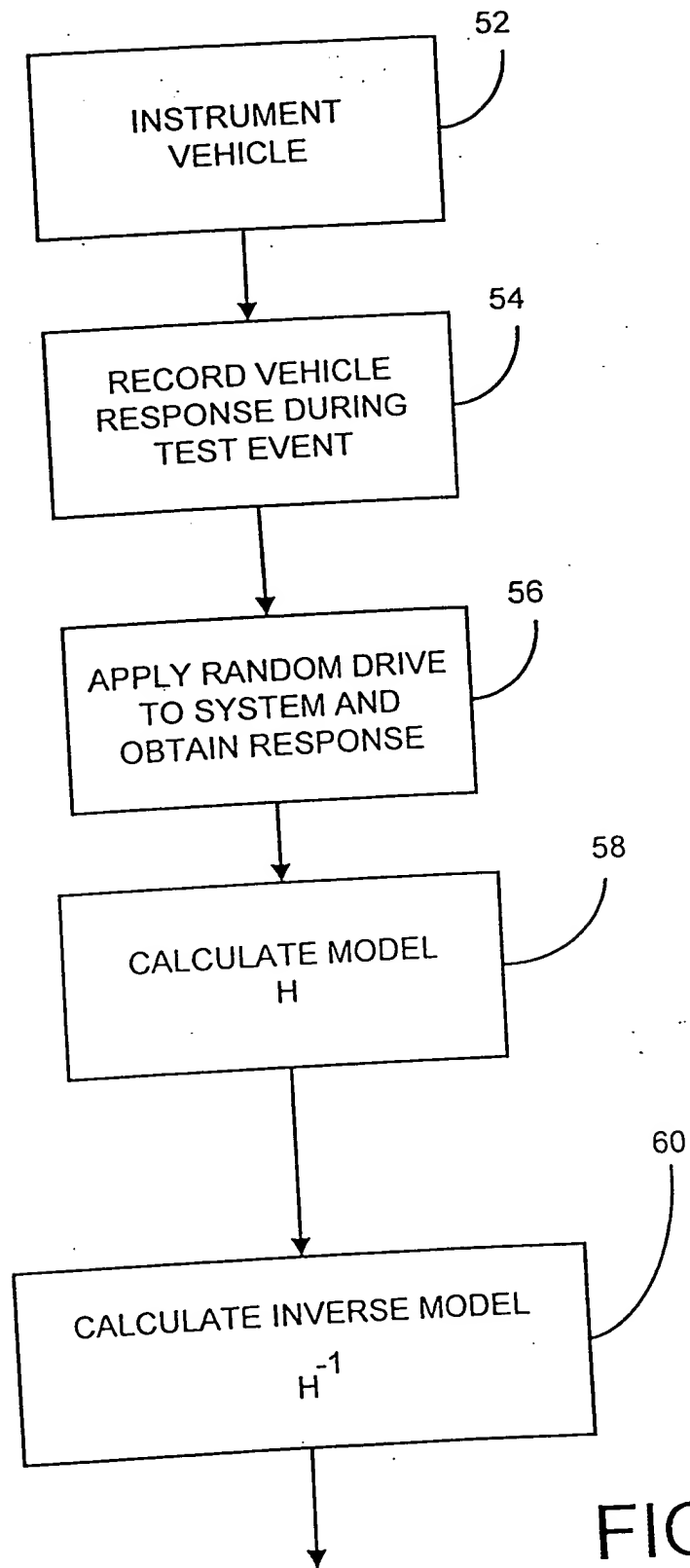


FIG. 3A

FIG. 3B
OR
FIG. 3C

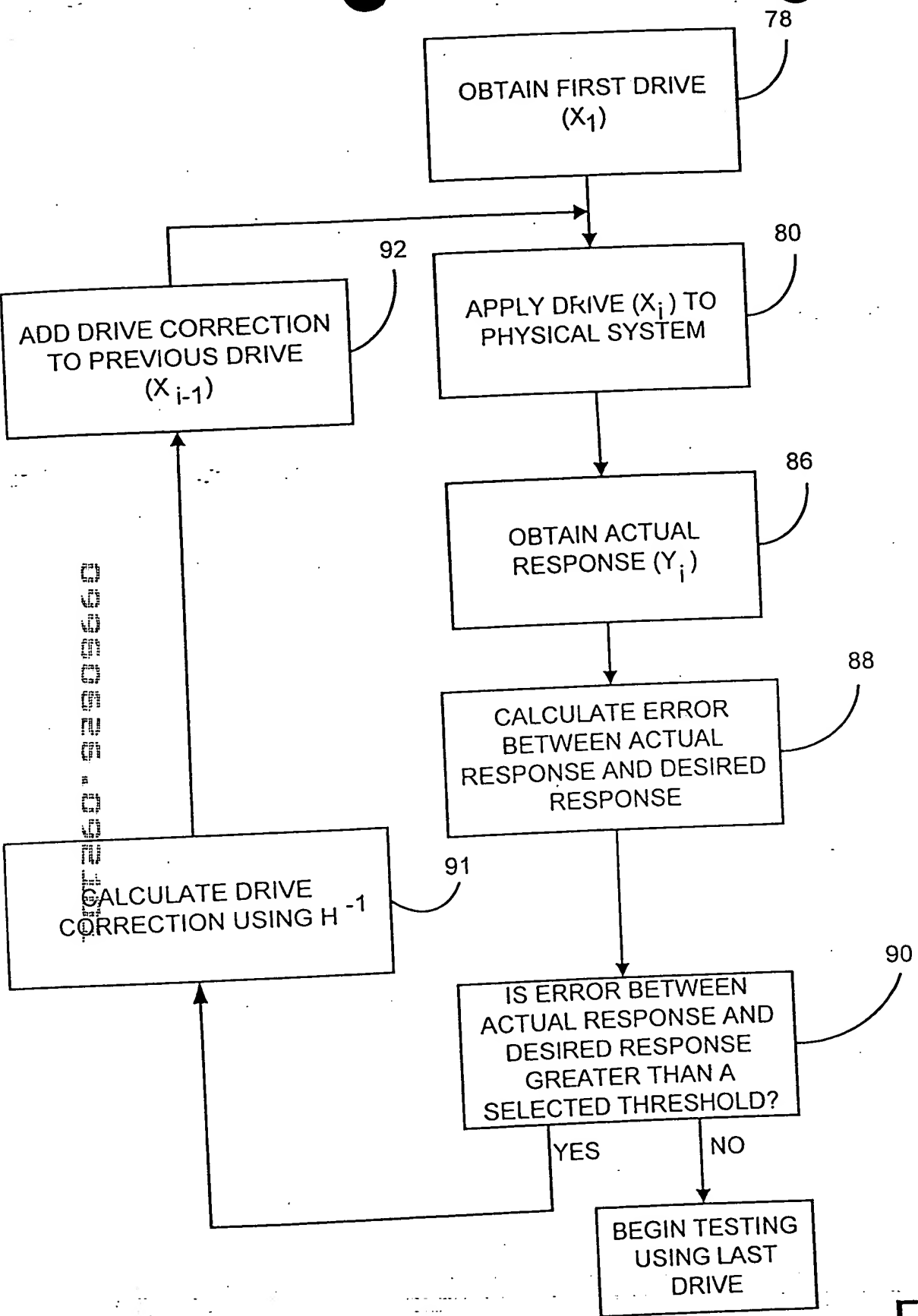


FIG. 3B

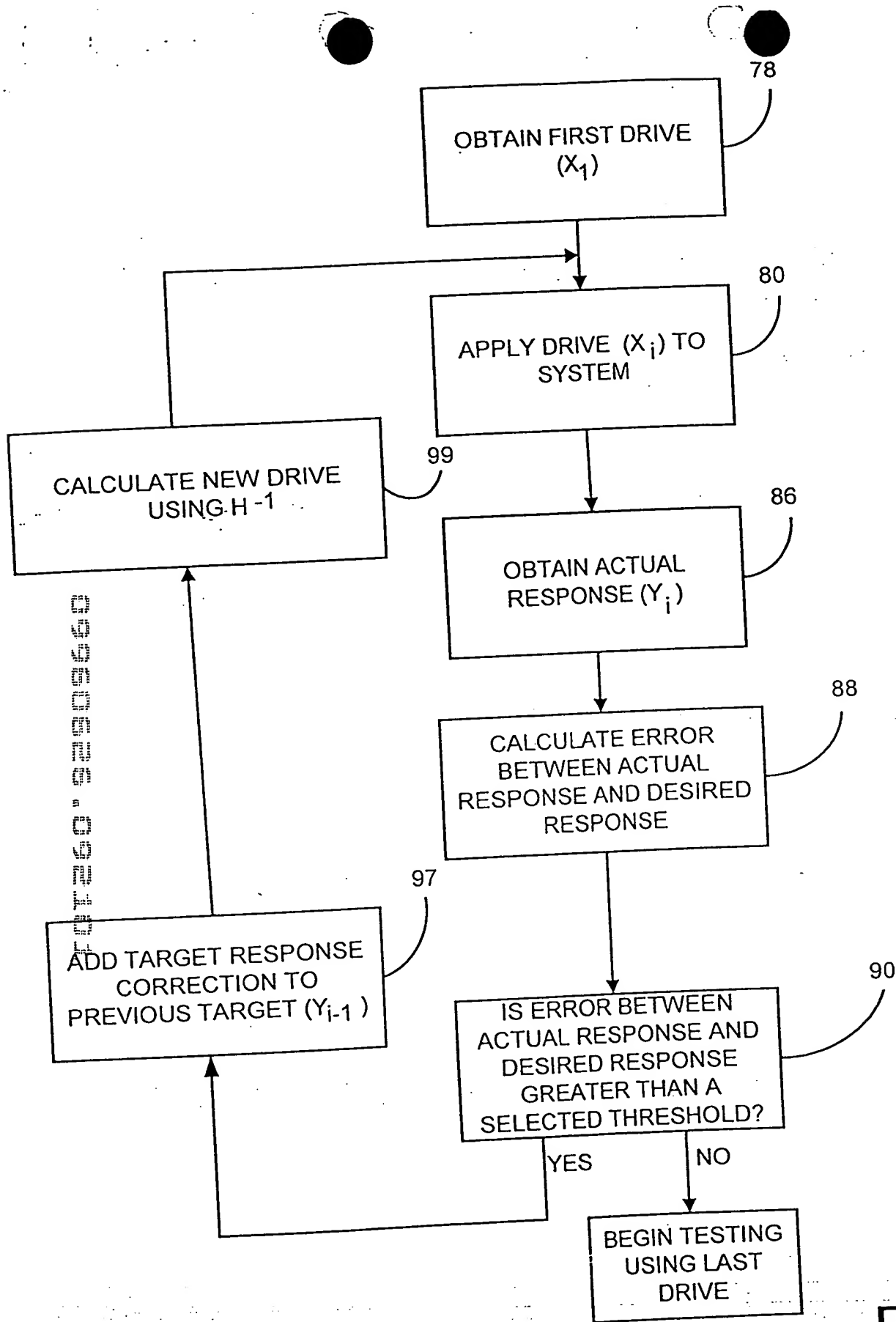


FIG. 3C

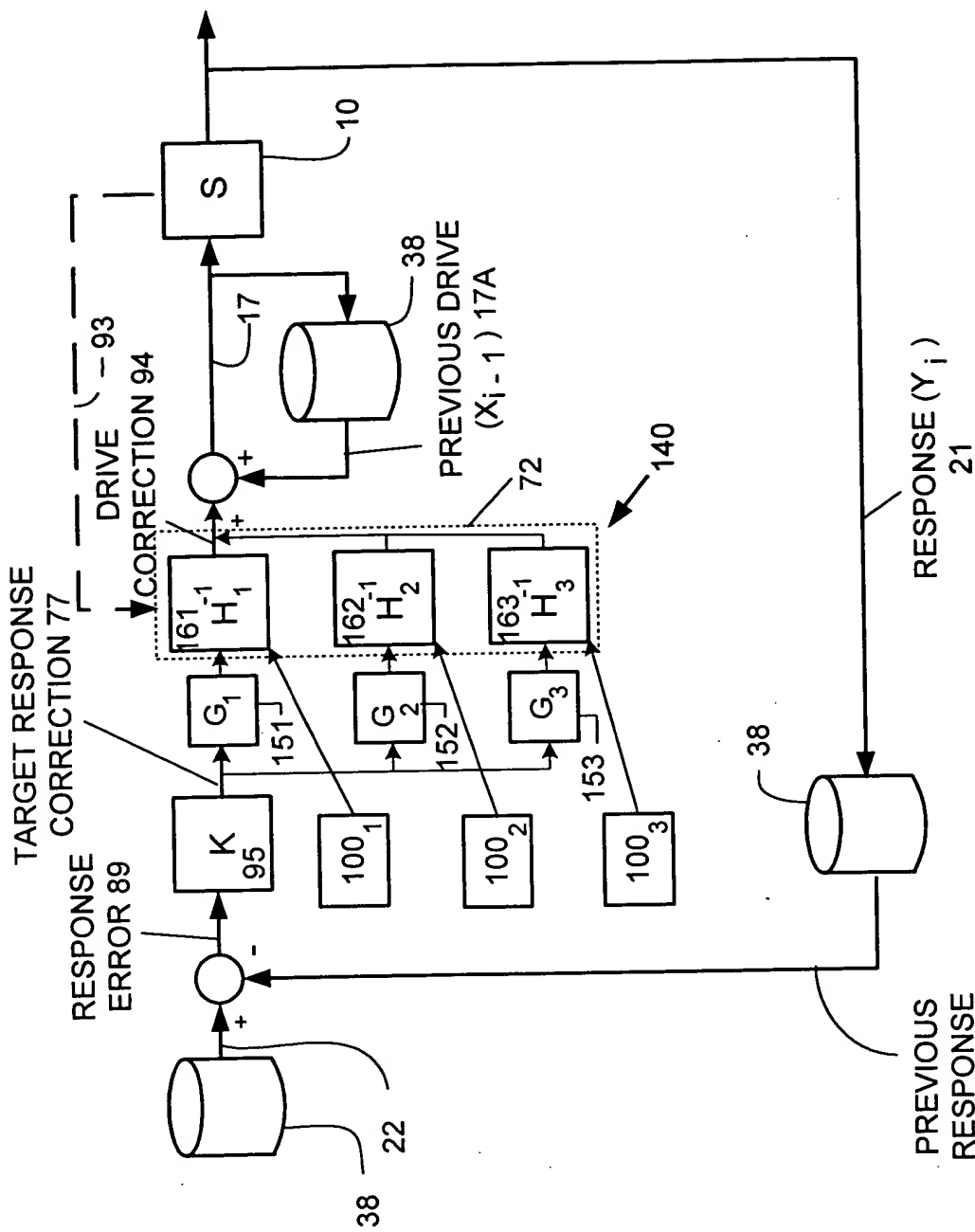


FIG. 4A

The diagram illustrates a control system 10. A reference input enters a summing junction (circle with a '+') and is also fed into a block labeled 'S'. The output of this summing junction is the 'RESPONSE ERROR 89', which is fed into a block labeled 'K'. The output of block 'K' is fed into another summing junction (circle with a '+'). This second summing junction also receives input from a 'PREVIOUS RESPONSE (Y_{i-1}) 87' and a 'PREVIOUS TARGET RESPONSE 79A' (from a cylinder 38). The output of this second summing junction is the 'TARGET RESPONSE 79', which is fed into a parallel arrangement of three paths. Each path consists of a block labeled 100₁, 100₂, or 100₃, followed by a block labeled G₁, G₂, or G₃, and finally a block labeled H₁⁻¹, H₂⁻¹, or H₃⁻¹. These H blocks are grouped within a dashed box labeled 140. The outputs of these three parallel paths are summed at a third summing junction (circle with a '+'). The output of this third summing junction is fed back to the 'PREVIOUS RESPONSE (Y_{i-1}) 87' and the 'PREVIOUS TARGET RESPONSE 79A' cylinders (38). The output of the third summing junction is also fed into the 'S' block. The output of the 'S' block is the final 'RESPONSE (Y_i) 21', which is fed back to the first summing junction and also to the 'PREVIOUS RESPONSE (Y_{i-1}) 87' cylinder (38).

FIG. 4B

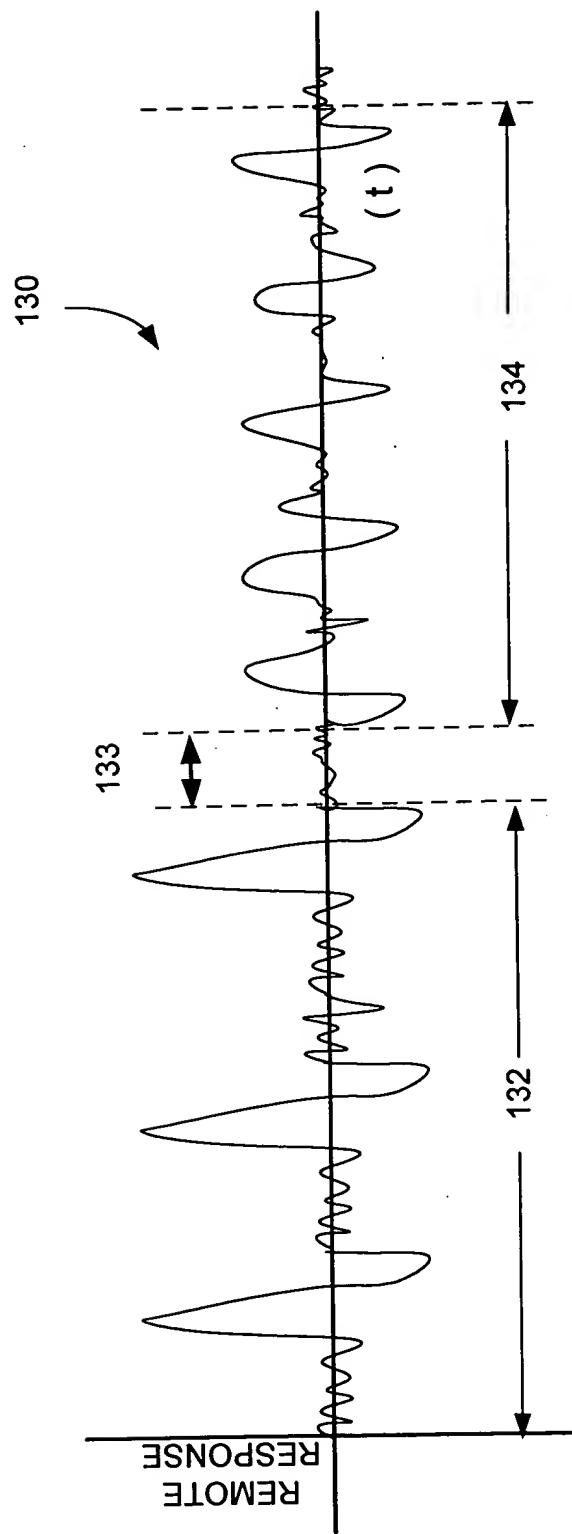


FIG. 5

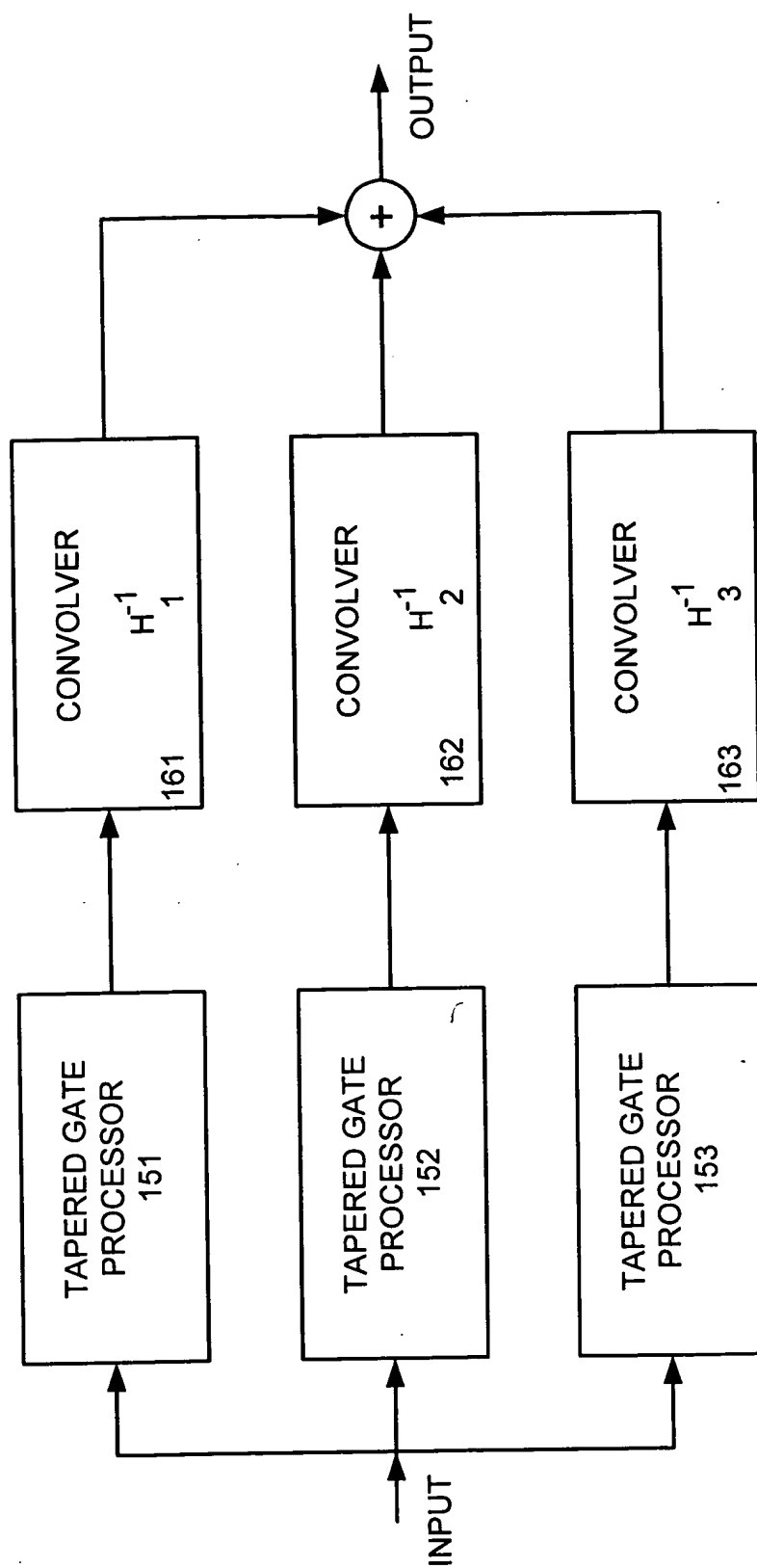


FIG. 6

101260-92999660

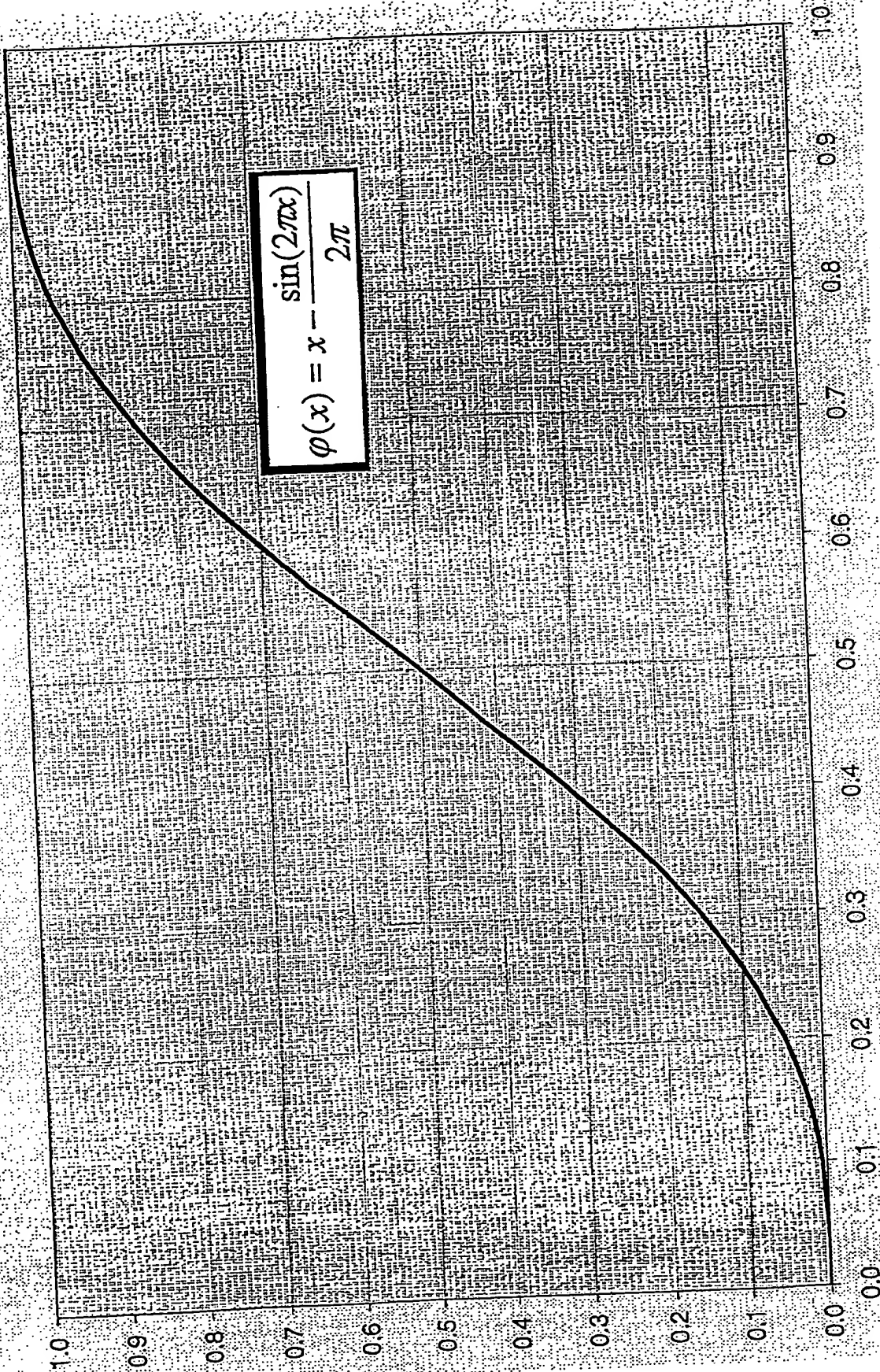


FIG. 7

09560628 0940 09560628

155

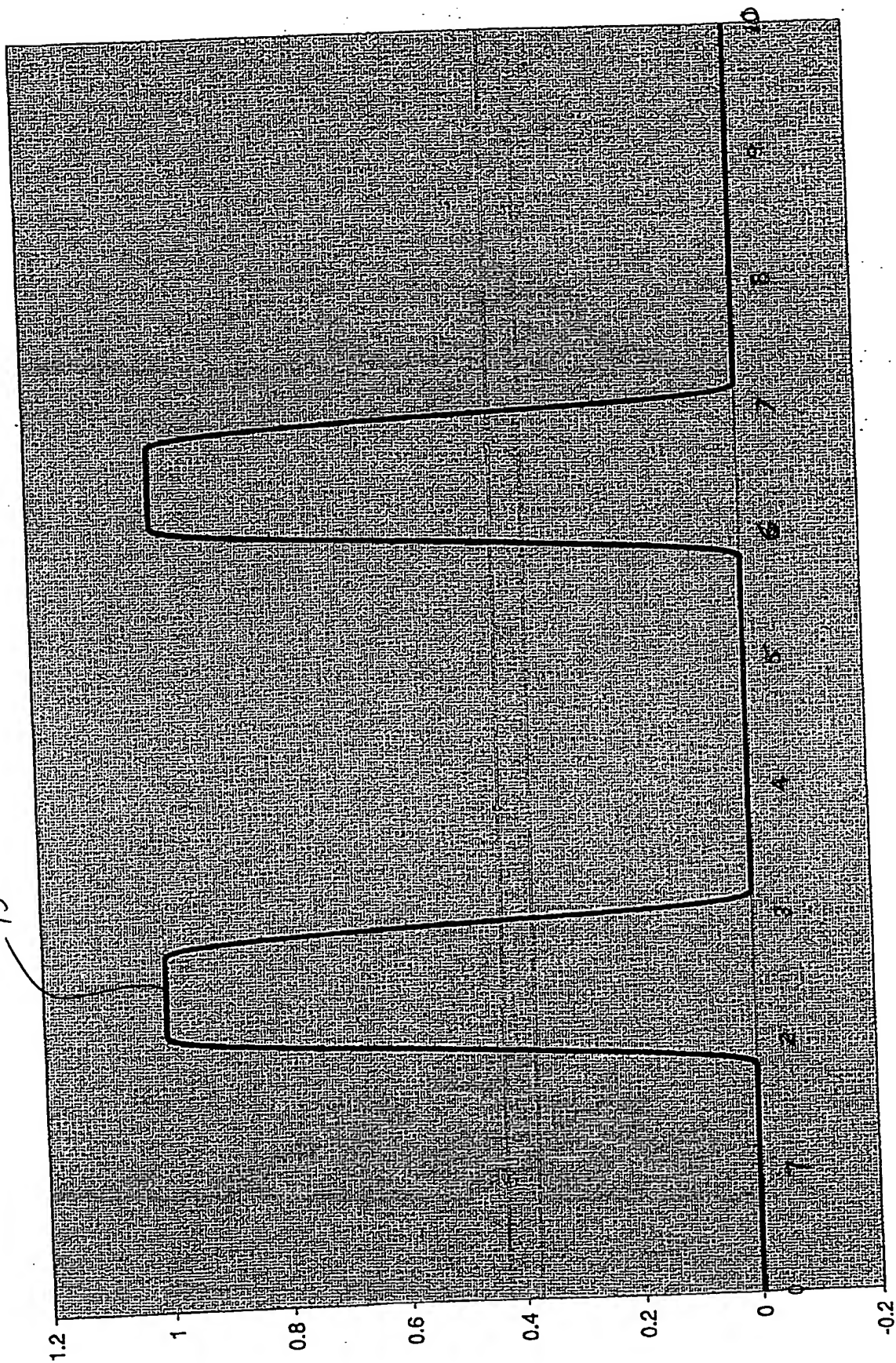


Fig. 8

101250" 2209560

157

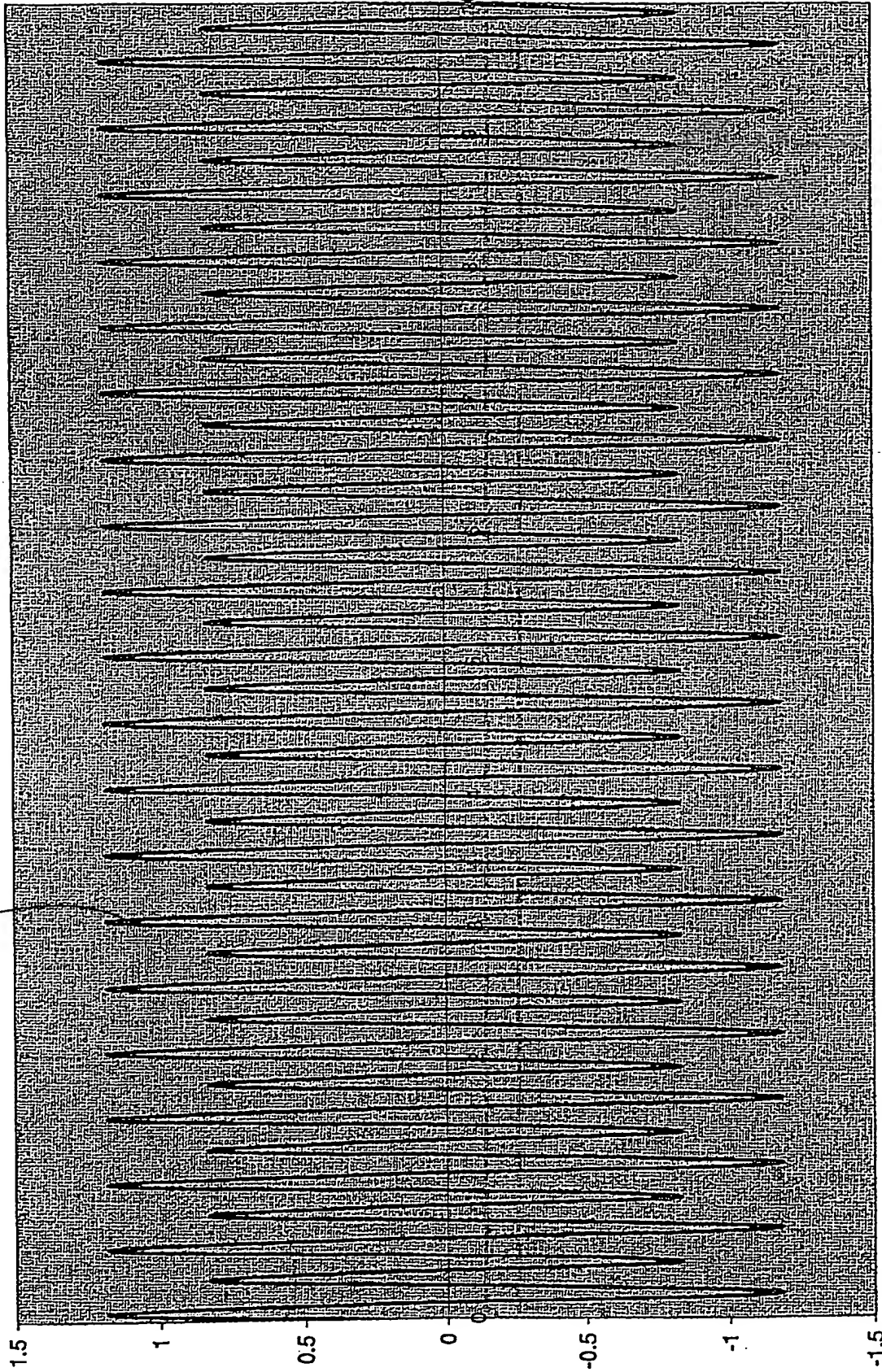


Fig. 9

FOI260 92909660

- Gated Input Signal

159

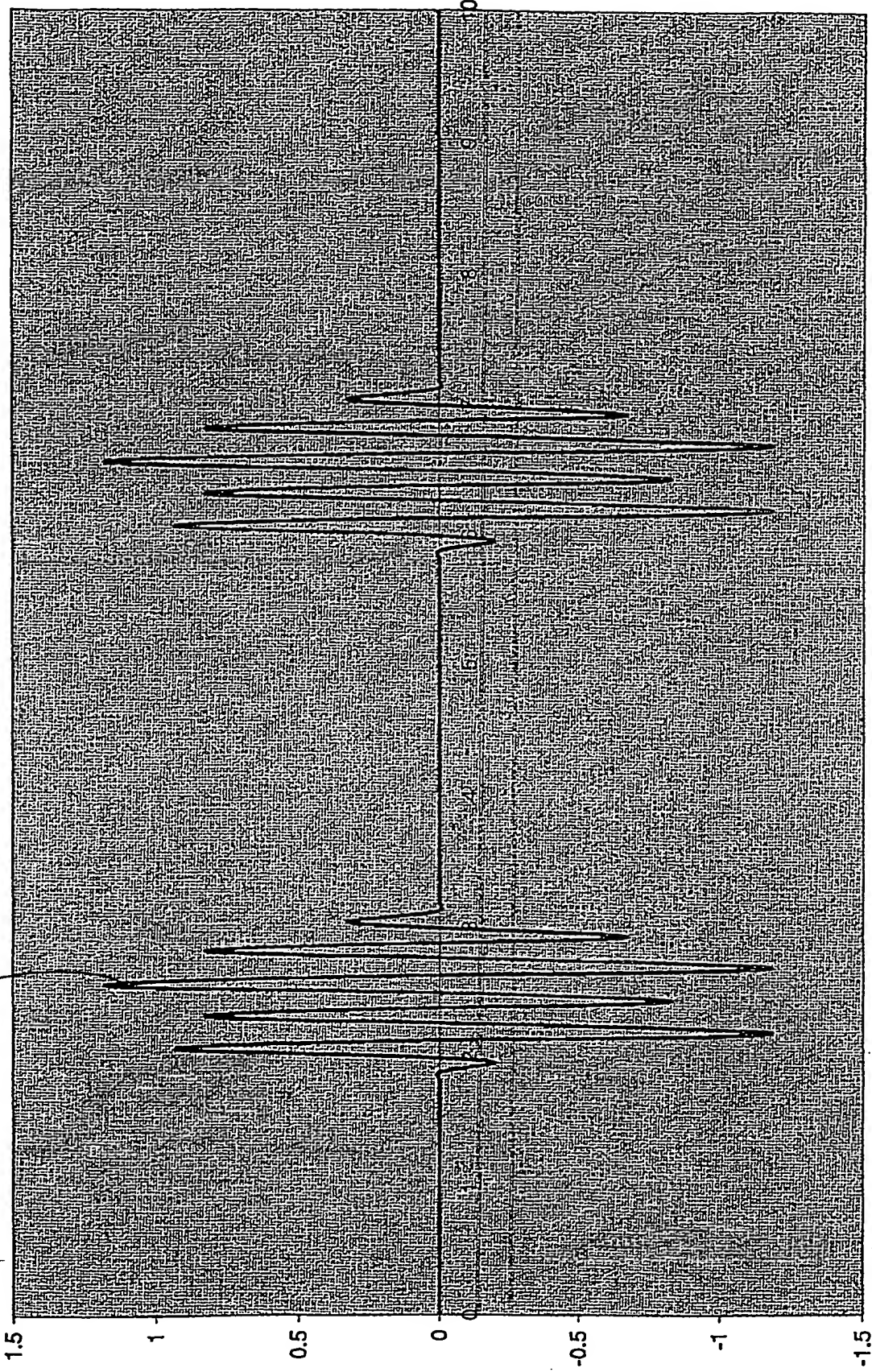


Fig. 10

FIG. 11

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